IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Applicant(s): | Steven E. Boor |) | |
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| Appln No.: | 10/797,507 |) | CONFIRMATION NO. 3966 |
| Filed: | March 10, 2004 |) | This Response to Notification |
| Title: | MODIFIABLE BUFFER CIRCUIT FOR MINIATURE MICROPHONE APPLICATIONS AND METHOD OF ADJUSTING THEREOF |))) | of Non-Compliant Appeal Brief was electronically filed on April 8, 2010 using the U.S. Patent and Trademark Office's EFS |
| Group | |) | Web |
| Art Unit: | 2614 |) | |
| Examiner: | Olaniran, Fatimat O. |) | |
| | |) | |
| Attorney Dock | xet: 8354/96255 |) | |

RESPONSE TO NOTIFICATION OF NON-COMPLIANT APPEAL BRIEF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

22242

Dear Sir:

Customer No.:

Pursuant to an Office Communication mailed March 10, 2010 as entered in the above-captioned matter, a Notice of Non-Compliant Appeal Brief has been entered. Box 4 of Form PTOL-462(rev.7-05) has been checked. An explanation at the end of this form stated that "Section V Summary of Claimed subject matter must identify and map all independent claims separately on appeal to spec. by pg. and line number or paragraph number and/or drawings if any (Claims 1, 15, and 20)."

Box 4 indicates that (a) The Brief does not contain a concise explanation of the subject matter defined in each of the independent claims involved in the Appeal, referring to the Specification by page and line number and to the drawings, if any, by reference characters; and/or (b) the fails to: (1) identify, for each independent claim involved in the appeal and for each dependent claim argued separately, every means plus function and step plus function under 35 U.S.C. 112, sixth paragraph, and/or (2) set forth the structure, material, or acts described in the

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specification as corresponding to each claimed function with reference to the specification by page and line number, and to the drawings, if any, by reference characters.

As there are no means plus function or step plus function claims presented in this appeal and as confirmed by the explanation section of the Notice, section 4(b) is not at issue.

The applicant below presents a concise explanation of the subject matter appears as follows in the form of claim subject matter maps with corresponding references to the specification as published¹ by paragraph numbering and to the drawings by figure number and reference characters.²

Independent claim 1

Fig. & Paragraph of spec.

| A buffer circuit (100) for use in a microphone assembly (312) comprising: | FIG. 1, FIG. 5, and FIG. 6 |
|---|----------------------------|
| an input (104) for receiving a signal; | FIG. 1 |
| | Paragraph 0015 |
| an input buffer (102) coupled to the input (104); | FIG. 1 |
| | Paragraph 0015 |
| an output (108); | FIG. 1 |
| | Paragraph 0015 |
| a filter network (106) coupled between the input buffer (102) and | FIG. 1 |
| the output (108); | Paragraph 0015 |
| a selector (112) comprising: | FIG. 1 |
| | Paragraph 0015 |
| a first input (234); | FIG. 1, FIG. 4 |
| | <u> </u> |

¹ Publication No. US20040179703.

² As stated, there are no means plus function (or step plus function) recitations in any of the claims involved in this appeal, and therefore there is no identification of any corresponding structure, material, or acts in the specification in this regard. It will be understood that this summarization of the claimed subject matter is, in fact, a "summary" and that the applicant does not represent or intend that this brief presentation, or the accompanying references to the drawings and the specification, comprises an exhaustive presentation in this regard. As always, the claims are to be viewed and interpreted in view of the context of the entire specification sans the Abstract.

| | Paragraphs 0015, 0018. 0019 |
|---|-----------------------------|
| a first output (230) responsive to the first input (234); and | FIG. 1, FIG. 4 |
| | Paragraph 0015, 0018, 0019 |
| a tuning circuit (110) coupled to the filter network (106) for | FIG. 1, FIG. 4 |
| adjusting a characteristic of the filter network, the tuning circuit (110) responsive to the selector (112), wherein the characteristic of the filer network (106) is adjusted using the first input (234). | Paragraph 0015, 0018, 0019 |

Independent claim 15

Fig. & Paragraph of spec.

| A hybrid circuit for buffering an audio signal comprising: | |
|---|--|
| a substrate having a first and second portion (318), the second portion (318) severable from the first portion; and | FIG. 1, FIG. 2, FIG. 4, FIG. 5 |
| | Paragraphs 0015, 0022 and 0031 |
| a buffer circuit (100) substantially disposed on the first portion of the substrate, the buffer circuit (100) comprising: | FIG. 1, FIG. 2, FIG. 4, FIG. 5 |
| | Paragraphs 0015, 0022 and 0031 |
| a first input (234) for coupling the audio signal; | FIG. 1, FIG. 2, FIG. 4, FIG. 5 |
| | Paragraphs 0015, 0018 and 0019 |
| a filter network (218) coupled to the first input (234); | FIG. 1, FIG. 2, FIG. 4, FIG. 5 |
| | Paragraphs 0015, 0017, 0018, and 0019 |
| an output coupled to the filter network (218); | FIG. 1, FIG. 2, FIG. 4, FIG. 5 |
| | Paragraphs 0015, 0018 and 0019 |
| a tuner (224) for adjusting the filter network (218); and | FIG. 1, FIG. 2, FIG. 3, FIG. 4, FIG. 5 |

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| | Paragraphs 0015, 0017, 0018, and 0019 |
|--|---|
| a controller (232) for altering a value of the tuner (224), the controller (232) having a second input (317), the second input (317) disposed on the second potion (318) of the substrate, | FIG. 1, FIG. 2, FIG. 3, FIG. 4, FIG. 5 |
| | Paragraphs 0015, 0018, and 0019 |
| whereby a tuning signal coupled to the second input (317) is used to adjust the tuner (224), thereby changing a transfer function of the buffer circuit (100). | FIG. 1, FIG. 2, FIG. 3, FIG. 4, FIG. 5 |
| | Paragraphs 0014, 0015, 0017, 0018, and 0019 |

Independent claim 20

Fig. & Paragraph of spec.

| A method for adjusting a buffer circuit for use in a microphone assembly comprising: | FIG. 1 |
|---|--|
| providing a desired response characteristic for the buffer circuit (100); | FIG. 1 Paragraphs 0014, 0015, 0016, 0022 |
| measuring an initial response characteristic of the buffer circuit (100); | FIG. 1 Paragraph 0022 |
| comparing the desired response characteristic to the initial response characteristic; | FIG. 1 |
| | Paragraph 0022 |
| determining an adjustment using the comparison, the adjustment | FIG. 1 |
| for reducing a difference between the desired and initial response characteristics; | Paragraph 0014, 0015, 0016, 0022, 0023 |
| transmitting a signal to a selector circuit in the buffer circuit (100); and | FIG. 1 |
| | Paragraph 0023 |
| tuning an adjustable filter coupled to the selector circuit, the adjustable filter for modifying the initial response characteristic. | FIG. 1, FIG. 3 |
| | Paragraph 0018, 0023 |

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The applicant submits that these mappings bring the brief into compliance with the requirements of the Board. If the Examiner should have any other specific concerns, the Examiner is invited to contact the undersigned by telephone to seek an appropriate accommodation.

Respectfully submitted,

FITCH, EVEN, TABIN & FLANNERY

Bv:

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